

<b>Notice of References Cited</b>	Application/Control No. 10/627,355		Applicant(s)/Patent Under Reexamination LLINAS ET AL.	
	Examiner PETER COUGHLAN		Art Unit 2129	Page 1 of 1

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	D	US-			
	E	US-			
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	'Microelectronic circuits': Sedra, Oxford University Press, 1998, 668-669, 714-716, 973-977
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.